

FIGURES

Figure C-1 Flow Diagram of Initial PCB PE Acceptance Limit Generation

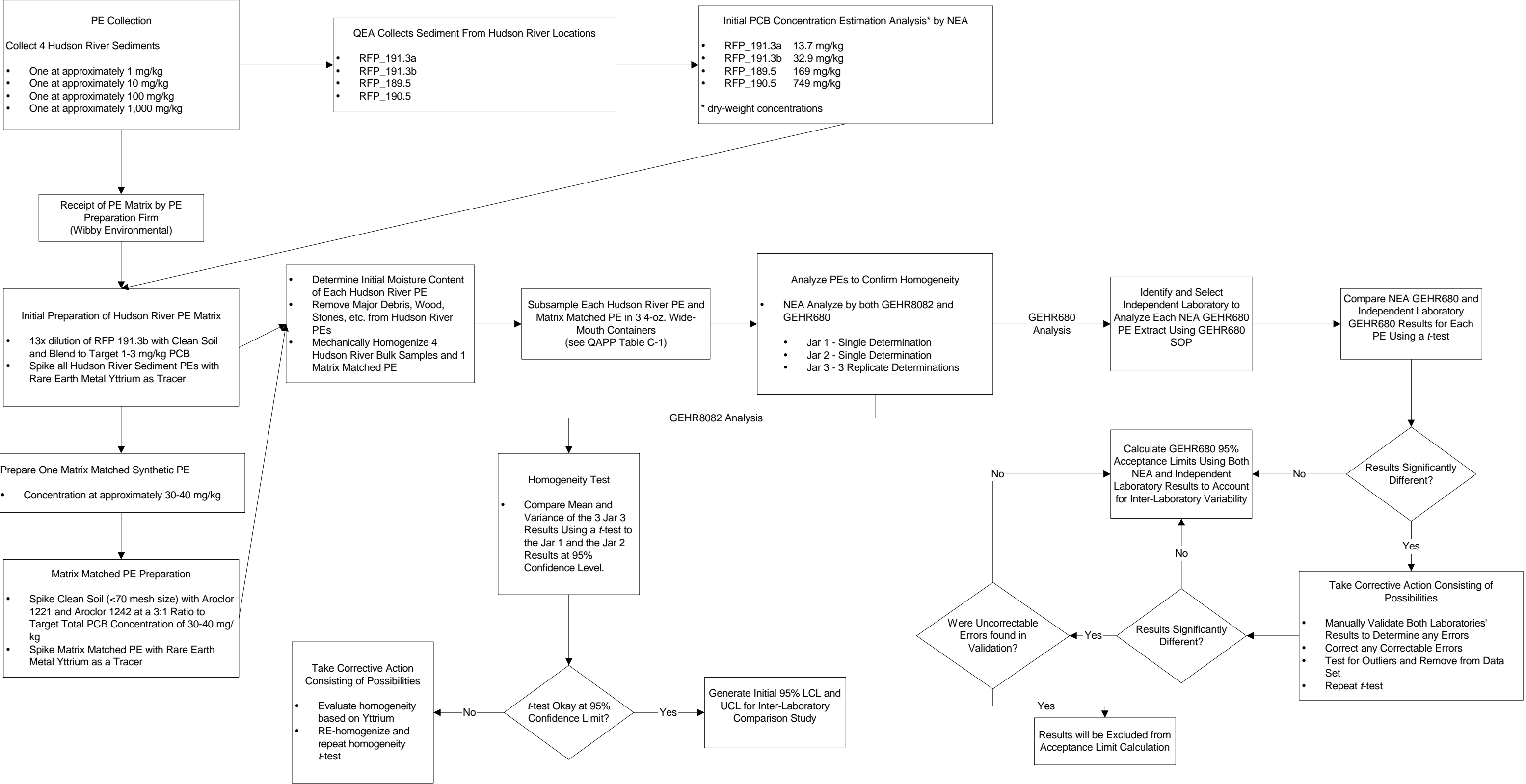


Figure C-2 Flow Diagram of Inter-Laboratory Comparison Study PE Analysis

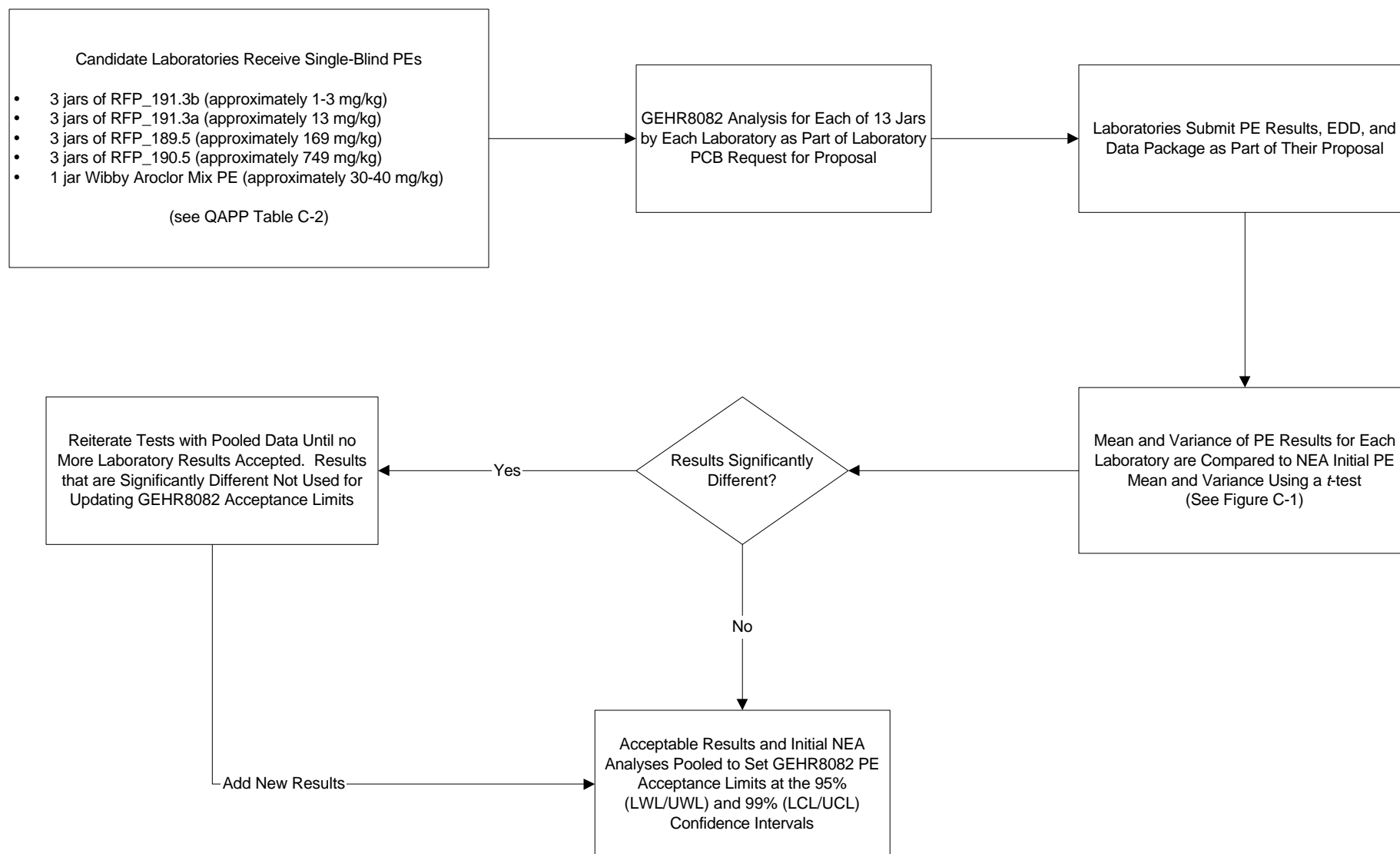


Figure C-3 Flow Diagram for GEHR8082 and GEHR680 PE Sample Analysis

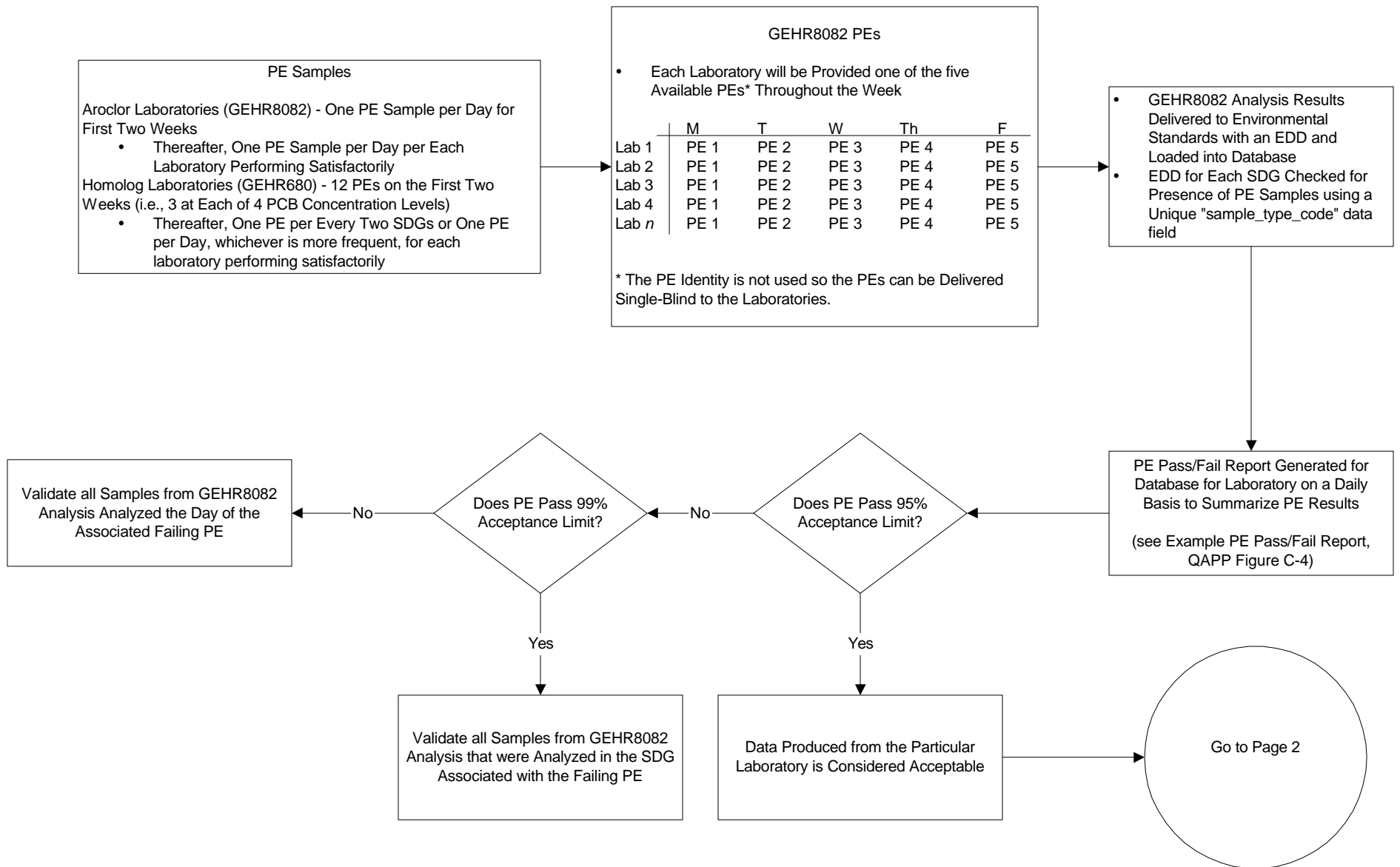


Figure C-3 Flow Diagram for GEHR8082 and GEHR680 PE Sample Analysis

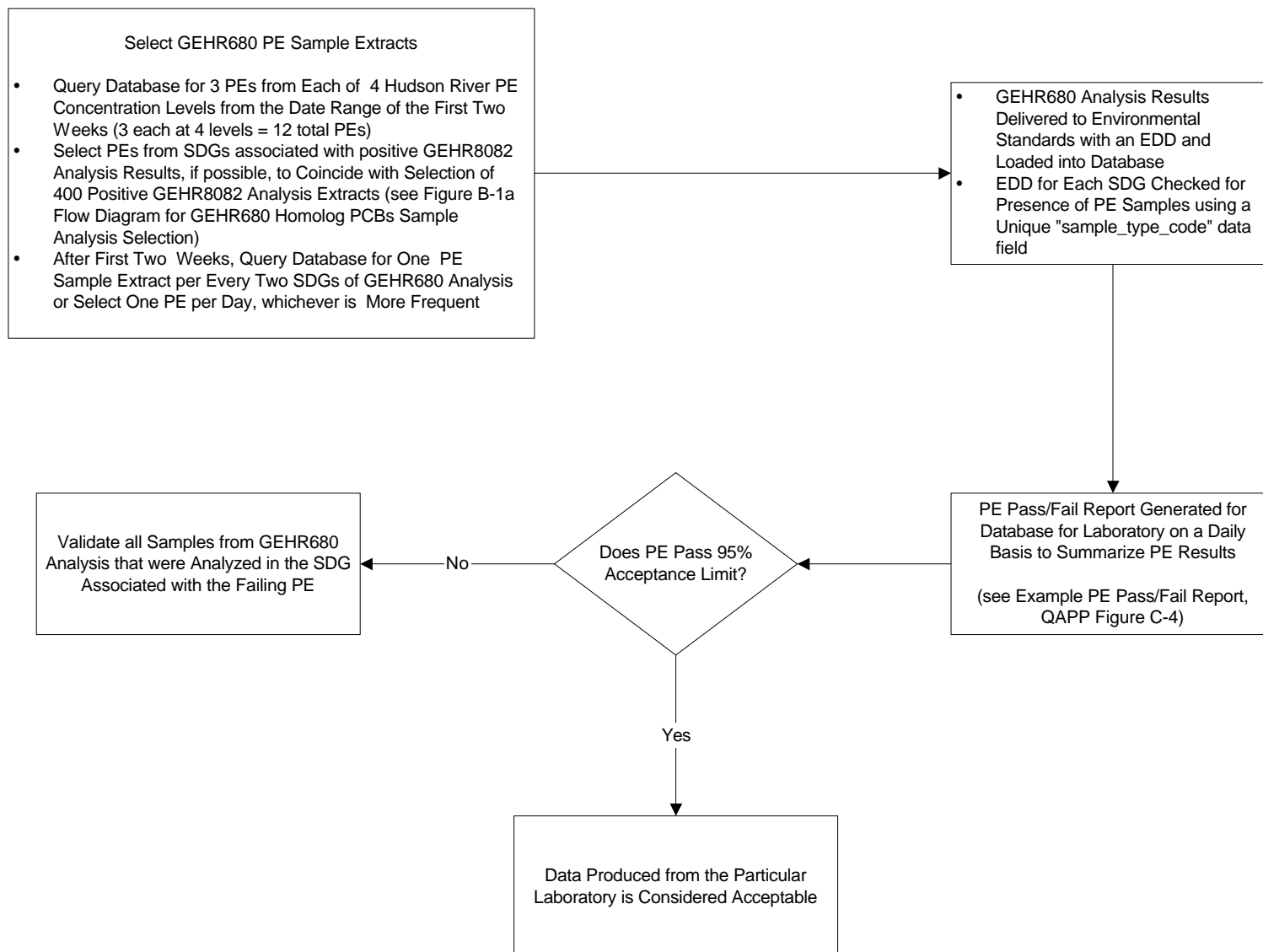


Figure C-4
Example PE Sample Result Pass/Fail Summary Report
GE Hudson River Design Support Sediment Sampling and Analysis Program

LABORATORY: _____ PE ID: _____ COC: _____ SDG: _____ DATE COLLECTED: _____ METHOD: _____								
	Result		95%		WL Qualifer	99%		CL Qualifer
	mg/Kg	MDL	LWL	UWL	P or F	LCL	UCL	P or F
Aroclor-1016								
Aroclor-1221								
Aroclor-1232								
Aroclor-1242								
Aroclor-1248								
Aroclor-1254								
Aroclor-1260								
Total PCBs								

Notes:

MDL= Method Detection Limit

LWL/UWL= Lower Warning Limit/Upper Warning Limit

LCL/UCL= Lower Control Limit/Upper Control Limit

P/F= Pass/Fail

If WL Qualifer=F then the SDG containing the PE sample must be validated.

If CL Qualifer=F then all associated SDGs must be validated.

Associated SDGs and COCs	Validation ? (Yes or No)
SDG 1/COC# Sample A Sample B Sample C	
SDG 2/COC## Sample D Sample E Sample F	
SDG 3/COC### Sample G Sample H Sample I	

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